

Modeling and Design of Spintronic Integrated Circuits

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Abstract—We present a theoretical and a numerical formalism for analysis and design of spintronic integrated circuits (SPINICs). The formalism encompasses a generalized circuit theory for spintronic integrated circuits based on nanomagnetic dynamics and spin transport. We propose an extension to the modified nodal analysis technique for the analysis of spin circuits based on the recently developed spin conduction matrices. We demonstrate the applicability of the framework using an example spin logic circuit described using spin Netlists.

Index Terms—Circuit theory, logic, magnetoelectronics, spin polarized transport, spintronics.

I. INTRODUCTION

SPINTRONICS, the technology of control and manipulation of the spin state of electrons and nanomagnets, is one of the most promising approaches for beyond CMOS logic, memory, and analog applications [1]–[5]. Several spin based devices have been proposed [6]–[19] with the possibility of logic-nonvolatility, intrinsic directionality, higher logical efficiency (large fan-in/fan-out) and reconfigurability. Combined with novel approaches for memory hierarchy [20], [21] and logic architecture [21]–[23], spintronics may enable high performance, normally-off (with zero standby power), and instantly-on computing engines.

In the past few years there has been tremendous progress in spintronic devices and integration [2], [24]–[30] propelled by the advances in materials and fabrication techniques. In particular, the advances towards in plane, three terminal and majority gate spin transfer torque devices [31]–[34] have opened the possibility of spin logic devices which enable computation to be performed entirely in the magnetic and spin states of materials. While the proposed devices show promising trends for non-volatile operation, low energy-delay products, and better logical efficiency, the suitability of the devices as components for large scale integration remains to be shown. In particular, significant advances in spin logic device, circuit and system design are still required in order to fully understand the suitability of spin devices for general purpose computing.

The goal of this paper is to outline the principles for analysis of integrated spintronic circuits so that the physics of spin transport can be utilized by SPICE developers and subsequently by circuit and system designers for the exploration of spintronics for “beyond CMOS computing.” We base the present work

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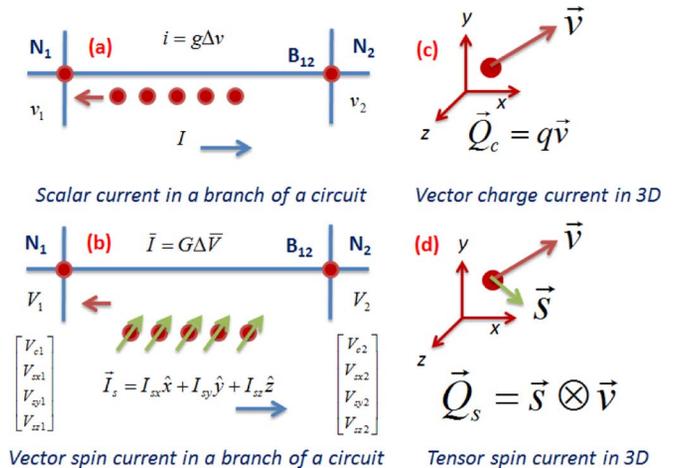


Fig. 1. Conceptual diagram of two nodes in a circuit connected by a conductance branch. (a) Two nodes connected by a scalar conductance in a regular circuit. (b) Two nodes connected by a spin conductance in a spin circuit. (c) Conceptual diagram of a spin current tensor when a spin current flows in a 3-D space. (d) Spin current tensor is reduced to a spin current vector when a direction is implied by a branch of the circuit. The linearity of the circuit implies that the connecting branch is described by a 4×4 spin conduction matrix.

on the physical principles for spin transport developed over the past few years, first in collinear magneto-electronics for spin valves [35] and then noncollinear magneto-electronics [36]–[38] for spin transfer torque devices. In particular, we use the formalism for 4×4 spin conduction matrices, introduced in [18], which enable the combined analysis of nanomagnets connected by spin transport channels. These were utilized for modeling of spin logic devices in [19]. We describe the circuit level transport models for spintronic devices from the physics of the spin transport through nanomagnets and nanochannels. We generalize the Kirchoff’s conservation laws for spin circuits to include spin dissipation [36]. We then extend the principles of modified nodal analysis (MNA) [62] to spin circuits enabling Netlist based SPICE simulations.

II. CONCEPTS OF VECTOR SPIN CURRENT, SPIN-VOLTAGE, AND SPIN CONDUCTION MATRICES

To develop the formalism for spin conduction, let us consider a branch of generic circuit consisting of two nodes N_1 , N_2 connected by a conduction element B_{12} [Fig. 1(a) and (b)].

We now describe the concept of vector spin current \vec{I}_s (with the three Cartesian components specified by three scalar numbers), vector spin voltage \vec{V}_s (with three Cartesian components specified by three scalar numbers), total current vector \vec{I} consisting of the coulomb current and vector spin current, total spin voltage \vec{V} (consisting of the coulomb voltage and vector spin voltage) [18].

A. Node of a Spin Circuit

We formally define the node of a spin circuit as a collection of physical points in a device or a circuit where all the quantities of interest for spin and charge transport are at equilibrium [36]. The mechanisms driving the node to the state of equilibrium are assumed to be much faster than the dynamics of the circuit.

B. Vector Spin Current

Vector spin current in a branch of a spin circuit, is the net vector flow of magnetic moment along the branch of the circuit. It has the units of amperes. In general, the spin current flowing in a three dimensional space is a tensor [37], [39]. The spin tensor is described by a direction of the flow of the charges constituting the spin current and the direction of the net magnetic moment (spin) of the charges along each axis of the Cartesian coordinates [Fig. 1(c) and (d)]. However, in a circuit, the direction of the flow of charges is defined by the connectivity of the branch and therefore, the spin current flowing between two points of a spin circuit is a vector.

$$\vec{I}_s = I_{sx}\hat{x} + I_{sy}\hat{y} + I_{sz}\hat{z}. \quad (1)$$

The spin current can be also related to the velocity and spin states of the carriers in a circuit/device. The components of the vector spin current are expressed as a sum over the momentum k states of electrons, normalized to density of electrons

$$I_{sj} = -e \sum_{A,k} Tr(\rho \hat{\sigma}_j) \nu_n, \quad (2.1)$$

where σ_j are Pauli matrices, A is cross sectional area, ν_n is the velocity component normal to it, and ρ is the spin density matrix. It is defined with the negative sign to reflect the negative charge of electrons, similarly to the electric charge current

$$I_c = -e \sum_{A,k} \nu_n. \quad (2.2)$$

This way, the spin current corresponds to the flux of magnetic moments. Thus, in Fig. 1(a), the electrical current (blue arrow to the right) is opposite to the flux of electrons (red arrow to the left). If the net spin projection is positive, then the spin current, blue arrow in Fig. 1(b), is opposite to the flux of electrons (red arrow).

C. Vector Spin Voltage

Vector spin voltage at a node

$$\vec{V}_s = V_{sx}\hat{x} + V_{sy}\hat{y} + V_{sz}\hat{z} \quad (3)$$

can be understood intuitively as the state variable associated with the accumulation of spins of a certain direction. It is related to the *half*-difference in the electrochemical potentials of the electrons with their spin up and down along the direction of this vector. On the other hand, this difference can be related to the *half*-difference of density of the electrons with spin up and spin down, Δn , following the Valet-Fert theory [1], [35]:

$$-e\vec{V}_s = \Delta\vec{\mu} = (\Delta\mu_x\hat{x} + \Delta\mu_y\hat{y} + \Delta\mu_z\hat{z}) = \frac{4}{3} \frac{\Delta\vec{n}}{n} \mu \quad (4)$$

where n is the total density, $\vec{\mu}$ is the electrochemical potential. Spin voltage is defined with the negative sign as well. It is done

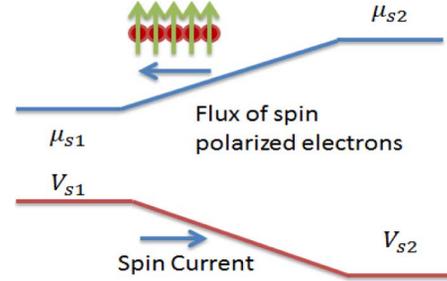


Fig. 2. Accumulation of spin up electrons is expressed in a higher spin electrochemical potential. Diffusion current of up spin electrons flows from a higher to a lower spin electrochemical potential. Equivalently, spin current flows from higher spin voltages to lower spin voltages.

to make the definition consistent to the usual electrochemical potential for electrons, which includes the term of voltage with a negative sign. Overall, this choice of signs in the definitions of spin current and spin voltage makes most of the relationships similar to those between charge current and voltage as in Fig. 2.

D. Total Spin Current Vector

The total spin current is simply the combination of the charge current and vector spin current. It is a 4×1 column vector.

$$\vec{I} = [I_c \quad I_{sx} \quad I_{sy} \quad I_{sz}]^T. \quad (5)$$

The ratio between the charge current and the magnitude of the spin current is the current's spin polarization ratio

$$\alpha_c = \left| \frac{\vec{I}_s}{I_c} \right|. \quad (6)$$

E. Total Spin Voltage Vector

The total spin voltage vector (V) is the combination of the scalar cumbic potential and the vector spin potential. It is a 4×1 column vector.

$$\vec{V} = [V_c \quad V_{sx} \quad V_{sy} \quad V_{sz}]^T. \quad (7)$$

The ratio between the scalar potential and the magnitude of the spin potential is the voltage-spin polarization ratio of a node.

$$\beta = \left| \frac{e\vec{V}_s}{\mu} \right| = \frac{4}{3} \frac{|\Delta\vec{n}|}{n}. \quad (8)$$

F. Spin Conduction Matrix

The concept of spin conduction matrix can be derived by postulating the linear response of current to voltage. This assumption is expected to be valid in metals, where electron density is high and electric fields change slowly compared to the scattering time. In the worst case, one has to take the branches of the circuit small enough to make it a good approximation. The spin Ohm's law, the linear relationship of spin voltage to spin current is

$$\vec{I} = G\Delta\vec{V} \quad (9)$$

where G is the 4×4 conductance matrix:

$$G = \begin{bmatrix} G_{11} & G_{12} & G_{13} & G_{14} \\ G_{21} & G_{22} & G_{23} & G_{24} \\ G_{31} & G_{32} & G_{33} & G_{34} \\ G_{41} & G_{42} & G_{43} & G_{44} \end{bmatrix}. \quad (10)$$

G can also be interpreted as a 4×4 tensor that scales and reorients the voltage vectors to obtain the spin current vector. Therefore, the spin conductance matrix of a conductance element is the matrix proportionality constant relating the vector spin current through an element with the vector spin voltage difference applied across a conductance element. In general the 16 components of a spin conduction matrix are nonzero and are set by the magnetic and geometric properties of the spin conductance element.

III. CONSERVATION LAWS FOR SPIN CIRCUITS

We now describe the extension of the Kirchoff's current and voltage laws to spin circuits [36]. Establishing the conservation laws is essential to setup a unique set of equations governing the currents and voltages.

A. Kirchoff's Voltage Law Extension for Spin Circuits

The traditional voltage law for circuits is extended straightforwardly to spin circuits: since the sum of voltage differences in any closed loop is zero

$$\sum_{i,j \in S_{\text{loop}}} \bar{V}_i - \bar{V}_j = 0 \quad (11)$$

where S_{loop} is the set of all node pairs in a given closed loop.

B. Kirchoff's Current Law Extension for Spin Circuits

Apart from the vectorial nature of spin currents and voltages, the difference between spin circuits and electric circuits is that charge is strictly conserved, but spin is not. We handle the non-conservative nature of the spin currents entering a node by introducing a spin dissipation current to a virtual ground [19]. The traditional Kirchoff's current law is thus extended to spin circuits as follows: the sum of the vector spin currents entering node is equal to the total dissipated vector spin current at the node. At node i , the spin node current law is given by (12), where \vec{I}_{ij} is the spin current from node i to node j ; B_i is the set of all nodes connected to node i ; $\vec{I}_{d,i}$ is the total spin current dissipated due to spin flip events happening at the node (I_{sf}).

$$\sum_{j \in B_i} \vec{I}_{ij} - \vec{I}_{d,i} = 0. \quad (12)$$

IV. 4-COMPONENT SPIN CONDUCTION MATRICES FOR NONMAGNETIC CHANNELS, SERIES, AND PARALLEL ELEMENTS

We describe spin conduction matrices for nonmagnetic elements (NME).

A. Spin Conduction Through Nonmagnetic Elements

The spin conduction through a linear nonmagnetic conductive element can be described as follows: a) the charge current through the device is directly proportional to the applied scalar voltage difference; b) the spin vector current through the element

is directly proportional and collinear to the vector spin voltage difference applied to the NME, i.e.,

$$I_c = g \Delta V_c \quad (13)$$

$$\vec{I}_s = g_s \Delta \vec{V}_S \quad (14)$$

where g_s is a scalar quantity, $\Delta \vec{V}_S$ is the applied spin vector voltage across the NME, g is the scalar conductance, ΔV_c is the applied voltage difference.

B. Spin Conduction Matrix for a Spin Elastic Series Branch

Using the concept of spin conduction through nonmagnetic elements, we write the conductance of a series resistor as [18]

$$G_{se} = \begin{bmatrix} g & 0 & 0 & 0 \\ 0 & g_s & 0 & 0 \\ 0 & 0 & g_s & 0 \\ 0 & 0 & 0 & g_s \end{bmatrix} \quad (15)$$

where we described the matrix elements earlier. A resistor with no spin flip is described fully by a single element as shown in (15). However, a series resistor with spin-flip needs to include a spin flip conductance to accommodate for the loss of spin current.

C. Spin Conduction Matrix for a Spin-Flip Conductance

We use the concept of spin-flip conductance to handle a non-spin-ballistic resistor. The conductance of a spin flip resistor is [18]

$$G_{sf} = \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & g_{sh} & 0 & 0 \\ 0 & 0 & g_{sh} & 0 \\ 0 & 0 & 0 & g_{sh} \end{bmatrix} \quad (16)$$

where g_{sh} is the spin flip conductance to accommodate the loss of spin polarization. The spin flip conductance sinks the spin current to a virtual spin ground to emulate spin current conservation even in presence of spin flip events.

D. Spin Conduction Model (Π -or T -Distributed Equivalent) for Distributed Channel With Spin Flip

The spin conduction model for distributed channels in Π -equivalent and T -equivalent networks can be expressed using the shunt and series conductances described above. Let us consider a nonmagnetic channel connecting two nodes of a spin circuit N_1 and N_2 , Fig. 4(a). The process of spin flip which causes a loss of spin current from the channel is modeled using shunt resistances which go to the spin voltage ground $[0 \ 0 \ 0 \ 0]^T$, Fig. 4(b). No charge current flows through the shunt elements since the conductance components G_{1i} are zero.

The equivalent conductances in the Π -equivalent circuit for a nonballistic elastic channel with cross section area of the channel A_{ch} , channel resistivity ρ_{ch} , the channel length L_c , spin-flip length of the channel material λ_s , is shown in Appendix A.

Depending on the ground conditions, a T -model [Fig. 4(c)] may be more convenient. The conversion from Π -equivalent to a T -equivalent is as follows (see Appendix B for details):

$$G_{seT} = G_{sf\pi} + 2G_{se\pi} \quad (17)$$

$$G_{sfT} = 2G_{sf\pi} + G_{sf\pi} G_{se\pi}^{-1} G_{sf\pi}. \quad (18)$$

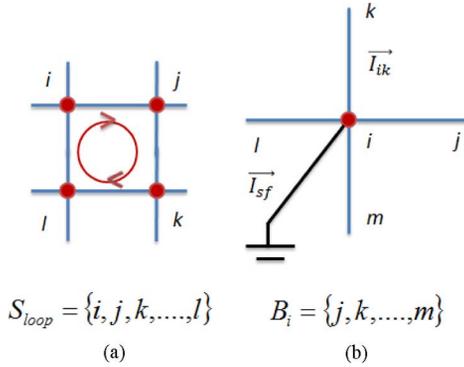


Fig. 3. Conservation laws for spin voltages and currents. (a) The sum of loop voltage differences is zero. (b) The sum of the physical spin currents from all physical branches is equal to the spin flip current to a virtual ground.

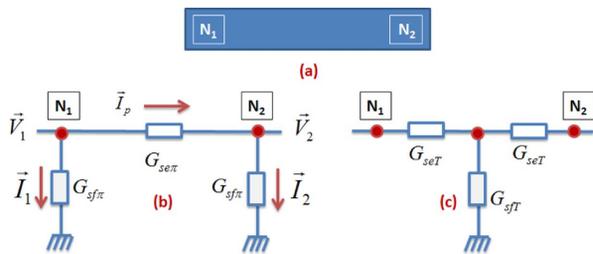


Fig. 4. Π and T Equivalent circuits for a normal metal distributed channel. (a) Normal metal channel connecting nodes N_1 and N_2 (b) Π equivalent circuit representing the distributed channel (c) T equivalent circuit representing the distributed channel.

V. 4-COMPONENT SPIN CONDUCTION MATRICES FOR MAGNETIC ELEMENTS

A. Spin Conduction Through Magnetic Elements

In contrast to spin conduction through nonmagnetic elements, spin conduction through magnetic elements in general can have: a) coupling between scalar voltages and spin currents and b) the spin current can be noncollinear to the vector spin voltage difference.

We next describe the conduction from a ferromagnet to a normal metal.

B. Spin Conduction at Ferromagnet and Normal Metal Interface

Spin conduction from a ferromagnet to a normal metal can be understood as the spin dependent current in response to spin voltages. The spin voltage at a node is in turn a result of a spin polarized population set up via spin injection from elsewhere [35]. Microscopically, the conduction is happening via spin dependent reflection and transmission at the interface of the ferromagnet and the normal metal [40], [41]. Much of the formalism is derived from quantum transport scattering theory, see e.g., [42], and draws on the work from superconductive transport [43]. We refer the reader to [37] for a detailed physical explanation.

We first derive the 4×4 conduction matrix of a ferromagnet (FM) to normal metal (NM) interface (Fig. 5) from the spin conduction equations [38]. The elements of the spin conduction matrix can be filled with experimental properties. Let the

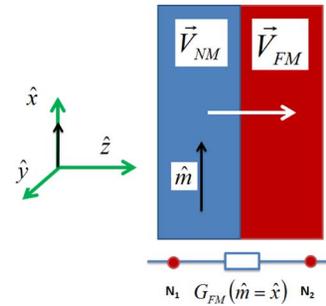


Fig. 5. Circuit model for spin transport between a ferromagnet (FM) and a normal metal (NM). The entire FM is treated as a node with a specific vector spin voltage.

4×1 spin voltages at the FM and NM be $\vec{V}_{FM} = [V_F; 0]$ and $\vec{V}_{NM} = [V_N; \vec{V}_{sp}]$ respectively. Here for simplicity we ignore the spin accumulation in FM. Let $\vec{I}_{NF} = [I_C; \vec{I}_s]$ be the 4×1 spin current from FM to NM and \hat{m} be the vector direction of the magnet's magnetic moment. Then, according to [38], the charge current is

$$I_C = G(V_N - V_F) + \alpha G \hat{m} \cdot \vec{V}_{sp}. \quad (19)$$

The total spin current is given by

$$\vec{I}_s = \vec{I}_{\parallel} + \vec{I}_{\perp} \quad (20)$$

$$\vec{I}_{\parallel} = G(\alpha(V_N - V_F) + \hat{m} \cdot \vec{V}_{sp})\hat{m} \quad (21)$$

$$\vec{I}_{\perp} = G_{SL}[\hat{m} \times [\vec{V}_{sp} \times \hat{m}]] + G_{FL}(\vec{V}_{sp} \times \hat{m}) \quad (22)$$

where \vec{I}_{\parallel} is the component of the spin current parallel to the magnetic moment and \vec{I}_{\perp} is the spin current perpendicular to the magnetic moment. The expressions for spin torque conductances G_{SL} , and G_{FL} , which are related to the spin reflection and transmission properties of the interface, are described in Appendix C.

The spin conduction matrix elements can also be deduced from experimental properties of the FM-NM interface. G is the value of the total conductance of the interface, α is the spin selectivity of the FM-NM interface. G_{SL} is the spin transfer conductance of the normal metal. The Sharvin conductance is the quantum limit of spin transfer conductance. G_{FL} is the field-like conductance term which is typically zero for many metal interfaces. It is encountered at higher voltages in tunneling barriers adjacent to ferromagnets.

C. Spin Conduction Matrix of a Fixed Nanomagnet

We derive the conduction matrix in a special case of the magnetization parallel to the plane of the interface, as shown in Fig. 5. We choose the coordinate system such that the x-axis is along the direction of magnetization $\hat{m} = \hat{x}$ and the other two axes form a right-handed coordinates. Then using projections to these coordinates, (19)–(22) become

$$I_C = G_{11}(V_N - V_F) + \alpha G_{11}V_{sx} \quad (23)$$

$$I_{sx} = \alpha G_{11}(V_N - V_F) + G_{11}V_{sx} \quad (24)$$

$$I_{s\perp} = G_{SL}(V_{sy}\hat{y} + V_{sz}\hat{z}) + G_{FL}(V_{sz}\hat{y} - V_{sy}\hat{z}). \quad (25)$$

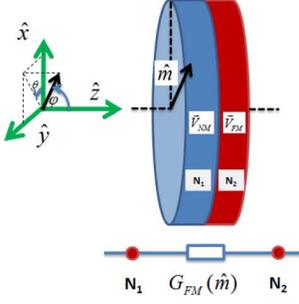


Fig. 6. Circuit model for spin transport between a ferromagnet (FM) and a normal metal (NM). The FM magnetization can point in any direction in three dimensions as determined by the nanomagnet dynamics.

TABLE I
LIST OF VARIABLES FOR SPIN CIRCUIT THEORY

Variable	Notation	Matrix Size	Units (SI)
Vector Spin Current	\vec{I}	3X1	A
Total Vector Spin Current	\vec{I}	4X1	A
Vector Spin voltage	\vec{V}	3X1	V
Total Vector Spin voltage	\vec{V}	4X1	V
Spin Conductance	G	4X4	Ohm ⁻¹
Spin Population Polarization	Δn	3X1	m ⁻³
Spin Current Polarization	α_c	1X1	Unit less
Spin Voltage Polarization	β	1X1	Unit less

TABLE II
TRANSPORT PARAMETERS USED IN SPIN CIRCUIT THEORY

Variable	Notation	Typical value	Units (SI)
Spin flip length of ferromagnetic metals	λ_s	5 (NiFe, Py) – 50 (Co) [31, 34, 44]	nm
Spin flip length of normal metals	λ_n	200-1000 [31, 34, 44-47]	nm
Spin current polarization	α_c	0.3-0.5 [34, 44-47]	-
Sharvin conductance of a normal metal	G_{sh}	0.47 (Co)-0.58 (Cu) [48]	Ohm ⁻¹ m ⁻²

Hence, the generalized Ohm's law for the FM-NM interface is [18]

$$\begin{bmatrix} I_c \\ I_{sx} \\ I_{sy} \\ I_{sz} \end{bmatrix} = \begin{bmatrix} G_{11} & \alpha G_{11} & 0 & 0 \\ \alpha G_{11} & G_{11} & 0 & 0 \\ 0 & 0 & G_{SL} & G_{FL} \\ 0 & 0 & -G_{FL} & G_{SL} \end{bmatrix} \begin{bmatrix} V_N - V_F \\ V_{sx} \\ V_{sy} \\ V_{sz} \end{bmatrix}. \quad (26)$$

Since (19)–(22) are independent of the orientation of the interface, we can extend by induction that the same expression for the conduction matrix is valid for any direction of magnetization provided that the coordinate system has its x-axis aligned to it, $\hat{m} = \hat{x}$

$$G_{FN}(\hat{x}) = \begin{bmatrix} G_{11} & \alpha G_{11} & 0 & 0 \\ \alpha G_{11} & G_{11} & 0 & 0 \\ 0 & 0 & G_{SL} & G_{FL} \\ 0 & 0 & -G_{FL} & G_{SL} \end{bmatrix}. \quad (27)$$

TABLE III
NANOMAGNET PARAMETERS FOR SPIN CIRCUIT THEORY USED IN COMBINATION WITH A MACROSPIN NANOMAGNET MODEL

Variable	Notation	Value/Typical Value	Units (SI)
Free Space Permeability	μ_0	$4\pi \times 10^{-7}$	JA ⁻² m ⁻¹
Gyromagnetic ratio	γ	17.6×10^{10}	s ⁻¹ T ⁻¹
Saturation Magnetization of the Magnet	M_s	10^6 [56]	A/m
Damping of the Magnet	α	0.007-0.01 [57-59]	-
Barrier Height	E_b	40-100 [60]	kT
Effective Internal Anisotropic Field	H_{eff}	10^3 - 10^6 [61]	A/m
Number of Bohr magnetons in the nanomagnet	N_s	10^3 - 10^6	-

D. Spin Conduction Matrix of a Free Layer Nanomagnet

Now we need the expression of the spin conduction matrix of the FM-NM interface in the fixed coordinate system tied to the nanomagnet shape (e.g., elliptical, see Fig. 6) rather than the instantaneous direction of magnetization. This derivation is given in Appendix D. The 4×4 conduction matrix in the fixed coordinate system given by

$$G_{FN}(\hat{m}) = R^{-1}(\hat{m})G_{FN}(\hat{x})R(\hat{m}) \quad (28)$$

where R is the rotation matrix

$$R(\hat{m}) = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & r_{22} & r_{23} & r_{24} \\ 0 & r_{32} & r_{33} & r_{34} \\ 0 & r_{42} & r_{43} & r_{44} \end{bmatrix}. \quad (29)$$

The elements of the rotation matrix R are defined by the expression of the unit vectors of the axes (X, Y, Z) tied to the magnetization in terms of the fixed coordinates (x, y, z)

$$[r_{22} \ r_{23} \ r_{24}] = \hat{X} = \hat{m} \quad (30)$$

$$[r_{32} \ r_{33} \ r_{34}] = \hat{Y} = -\frac{(\hat{X} \times \hat{x})}{\|\hat{X} \times \hat{x}\|} \quad (31)$$

$$[r_{42} \ r_{43} \ r_{44}] = \hat{Z} = \hat{X} \times \hat{Y}. \quad (32)$$

VI. SELF-CONSISTENCY OF NANOMAGNET DYNAMICS WITH SPIN CIRCUIT ANALYSIS

We now describe a coupled spin transport-magnetization dynamics model [76] for solving spin integrated circuits which employ nanomagnets for spin injection. The phenomenological equation describing the dynamics of nanomagnet with a magnetic moment unit vector (\hat{m}), the modified Landau-Lifshitz-Gilbert (LLG) equation [49], [50], with spin transfer torques in the form of [38] is (see Table III for parameters)

$$\frac{\partial \hat{m}}{\partial t} = -\gamma \mu_0 [\hat{m} \times \vec{H}_{eff}] + \alpha \left[\hat{m} \times \frac{\partial \hat{m}}{\partial t} \right] + \frac{\vec{I}_\perp}{eN_s} \quad (33)$$

where γ is the electron gyromagnetic ratio; μ_0 is the free space permeability; \vec{H}_{eff} is the effective magnetic field due to material/geometric/surface anisotropy; α is the Gilbert damping of

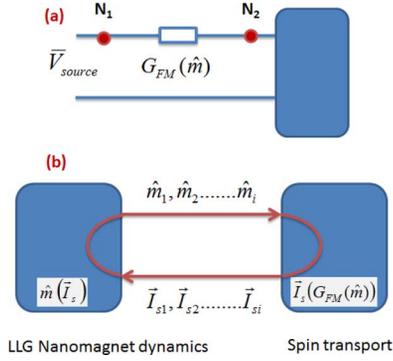


Fig. 7. Self-consistency of nanomagnet dynamics with spin transport. (a) Example circuit demonstrating the need for self-consistency. (b) Self-consistency between LLG dynamics and spin transport.

the material and \vec{I}_\perp is the component of vector spin current perpendicular to the magnetization (\hat{m}) entering the nanomagnet, N_s is the total number of Bohr magnetons per magnet. \vec{I}_\perp can also be rewritten as $\vec{I}_\perp = \vec{I}_s - \hat{m}(\hat{m} \cdot \vec{I}_s) = \hat{m} \times (\vec{I}_s \times \hat{m})$. The positive sign of the spin torque term is related to the fact (Section II-B) that the spin current is has the same direction as the flux of magnetic moments. Implicit in the LLG equation is the fact that absolute values of the magnetic moments of single domain nanomagnets remain constant. The noise properties of nanomagnets play a critical role in the dynamics of the magnets [51]–[54]. See Appendix F for a description of the noise properties and numerical methods for stochastic LLG equations.

In general, the direction of the nanomagnet magnetic moments of a spin circuit and the spin transport via a spin circuit are coupled together. The spin current entering a nanomagnet is defined by the conductance of the nanomagnet at the present angular position. This is because the equivalent conductance of the nanomagnet is determined by the direction the nanomagnet's moment. Consider an example spin circuit shown in Fig. 7(a). The current passing through the circuit depends on the direction of the magnet (\hat{m}) while the direction of the magnet is modified depending on the injected spin current. Hence at each instant of time a self-consistent solution needs to be calculated to ensure accuracy. Fig. 7(b) shows the self-consistent loop between LLG nanomagnet dynamics and spin transport [76]. The LLG solvers pass the condition of the magnets to the spin circuit and the spin circuit solver passes the spin vector current to the LLG solver at each pass of the self-consistent loop till a solution is reached. Self-consistency can also be addressed by using an implicit numerical solver [55].

VII. MODIFIED NODAL ANALYSIS FOR SPIN CIRCUITS

We now extend the modified nodal analysis (MNA) to spin circuits in order to provide a scalable way to analyze multi-node systems. A computational method for solving spin circuits is essential even for few node circuits (nodes > 2) since the ordering of the spin conduction matrices is crucial while calculating equivalent conductances (see Appendix G). Following closely the formalism for MNA [62], the spin-MNA solves the following equation:

$$[X] = [A]^{-1}[Z] \quad (34)$$

where A is a matrix formed based on the connectivity of the circuit, location of the voltage and current sources; X is the vector comprising unknown node voltages and unknown currents through the voltage sources; Z is the vector comprising of

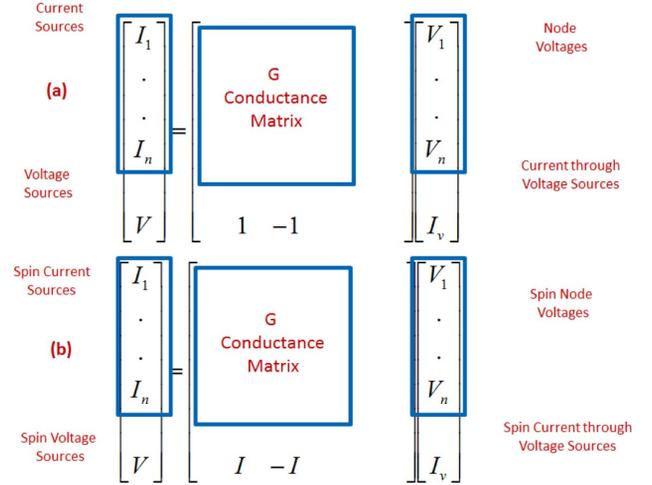


Fig. 8. (a) Constitution of the MNA matrix equation for normal circuits (b) Constitution of the Spin MNA matrix equation for spin circuits.

the voltages of the voltage sources and currents at the current sources. A can be also be written as

$$A = \begin{bmatrix} G & B \\ C & D \end{bmatrix} \quad (35)$$

where matrix A has the size $4(m+n) \times 4(m+n)$ (n is the number of nodes, and m is the number of independent voltage sources). Matrix G has the size $4n \times 4n$ and is determined by the interconnections between the passive circuit elements. Matrix B has the size $4n \times 4m$ and is determined by the connection of the voltage sources. Matrix C has the size $4m \times 4n$ and is determined by the connection of the voltage sources. (B and C are closely related, particularly when only independent sources are considered). Matrix D is $4m \times 4m$ and is zero if only independent sources are considered.

We note that the spin currents entering the magnets can be extracted as the Cartesian components of the total spin current. For a magnet connected between i^{th} and j^{th} nodes, the spin current entering i^{th} node

$$\vec{I}_{ij} = G(i, j)(\vec{V}_i - \vec{V}_j). \quad (36)$$

For a description of the rules/algorithm for writing matrix A please see Appendix H [63]. Matrix A can be assembled as shown in Fig. 8. The proposed method can handle a combination of nonmagnetic and magnetic elements as well as dependent and independent spin/regular voltage and current sources. For a detailed description on the regular MNA algorithm and dependent sources please see [64], [65]. The solution of the spin MNA equation can be simplified by optimal ordering of the equations to obtain sparsity [66] among other optimization techniques. For an algorithm for parsing a netlist to do MNA, see for example [65].

VIII. SIMULATION OF AN EXAMPLE SPIN CIRCUIT

We now describe an example spin circuit with embedded nanomagnets to demonstrate the effectiveness of the proposed framework. Let us consider a nonlocal spin injection detection device shown in Fig. 9 [19], [31], [34]. We consider a lateral spin injection-detection device which has been proposed recently as an all spin logic device. The device consists of two nanomagnets communicating via a nonmagnetic channel. The device operates

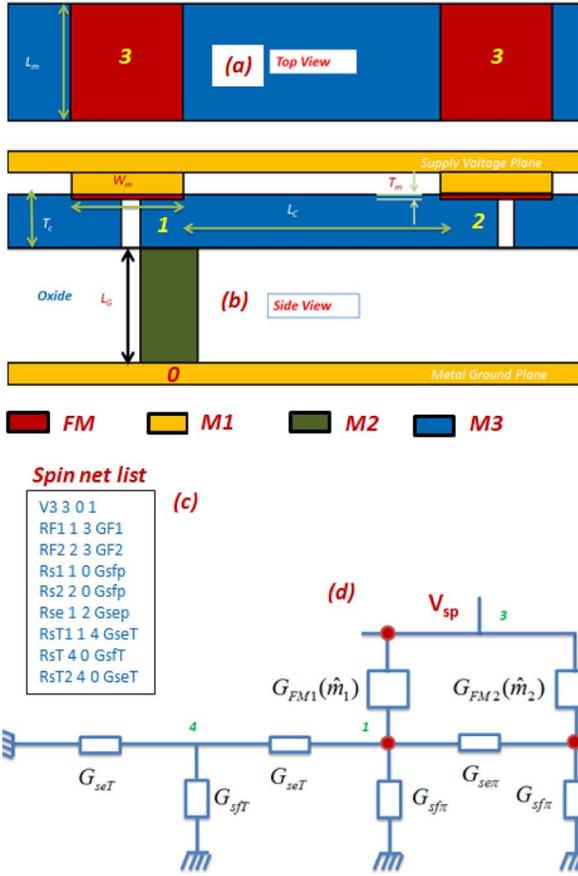


Fig. 9. A lateral spin logic device comprising two nanomagnets and nonmagnetic channels. Channel connecting 1–2 acts as an interconnect between the two magnets transporting spin polarized currents. (a) Top view of spin logic device. (b) Side view of a lateral spin logic device. (c) Netlist of the circuit that can be parsed by a spin-MNA algorithm. (d) Circuit model of spin logic device.

as an inverting gate for positive applied voltages and a noninverting gate for negative applied voltages.

Intuitively, the operation of the device can be explained as follows: the magnets create spin polarized population densities underneath the magnets and setup spin diffusion currents through the channel. The direction of this spin diffusion current is set by the relative strength of the spin polarization of the carriers. For a ground terminal set near the input magnet it can be shown that the magnet 1 acts as a fixed magnetic terminal, while the second magnet responds to the spin diffused to beneath it, depending on the applied voltages. For a positive applied voltage, the device shown in Fig. 9 acts like an inverting gate, where the output becomes a logical invert of the input. For negative applied voltages, the output becomes a copy of the input magnet's condition. The sectioned structure of the channel is required to isolate spin logic gates, where the interconnection between gates (concatenation) is achieved via a continuous free layer magnet. The nonreciprocity of (output to input signal transport) spin logic comes from an asymmetry between input and output magnets. This asymmetry can be achieved via: a) an asymmetric overlap of the magnet such that the area of the output is smaller than the area of input magnet; b) asymmetric ground condition; c) asymmetric spin injection efficiency (Fig. 9); d) asymmetric spin damping constant.

TABLE IV
SPIN MNA MATRICES

Variable	Size in MNA	Size in Spin-MNA
X	$(m+n)X(m+n)$	$(4m+4n)X(4m+4n)$
A	$(n+m)XI$	$(4n+4m)XI$
Z	$(n+m)XI$	$(4n+4m)XI$
G	nXn	$4nX4n$
B	nXm	$4nX4m$
C	mXn	$4mX4n$
D	mXm	$4mX4m$

TABLE V
PARAMETERS USED FOR EXAMPLE CIRCUIT SIMULATION

Variable	Notation	Value	Units (SI)
Saturation Magnetization of the Magnet	M_s	10^6	A/m
Damping of the Magnet	α	0.007	-
Effective Internal Anisotropic Field	H_{eff}	3.06×10^4	A/m
Stability of the magnet	Δ/kT	40	
Number of Spins	N_s	$10^3 - 10^6$	-
Thickness of Magnet	T_m	3	nm
Width of Magnet	W_m	37.8	nm
Length of Magnet	L_m	75.7	nm
Length of channel	L_c	100	nm
Thickness of channel	T_c	200	nm
Length of ground lead	L_g	200	nm
Thickness of ground lead	T_g	100	nm
Channel conductivity	ρ	7×10^{-9}	$\Omega \cdot m$
Sharvin conductivity	G_{sh}	0.5×10^{15}	$\Omega^{-1} \cdot m^{-2}$
Polarization	α_c	0.8	

A. Example Spin Circuit: Numbering the Nodes, Forming Spin Netlist

We model the device as a spin circuit comprising of two nanomagnets and nonmagnetic conductive elements. The nonmagnetic elements model the behavior of the metal channels connecting the magnets to each other and to the ground. In Fig. 9, we show the top view and side view of the device. We choose the node-0 to be the ground and number the remaining nodes as per the convention of MNA. Node 1, 2 represent the points in the device just below the magnets representing the ends of the channel. Node 3 is common node shared by the magnets and the supply. We can now derive the circuit diagram for the device as shown in Fig. 9(d). The magnetic elements are represented by G_{FM1} & G_{FM2} ; the nonmagnetic channel is represented by a Π -equivalent circuit as described in Section IV-D. The ground connection branch B_{10} is represented by a T equivalent circuit. The assumed dimensions and the list of variables are shown in Table V. We show the netlist for the device in Fig. 9(c).

B. Example Spin Circuit: Forming the MNA Equation

Using the rules described in Appendix H, we can build the spin-MNA equation for the circuit in Fig. 9(d) as follows: the G matrix is filled with the spin conductivity elements connecting to nodes 1–4. The elements representing connectivity $A(5,3)$ is filled with an identity matrix such that the applied voltage at node 3 is V_{sp} . The row $A3$ represents the KCL at the node 3 and

correspondingly has an identity matrix at element $A(3,5)$. See (37) at the bottom of the page.

C. Example Spin Circuit: Self-Consistent Solution of Spin-MNA With LLG Equations

The self-consistent solution is obtained by solving all the nanomagnet dynamical equations consistently with the transport equations (Fig. 8). The full set of equations for this example system then becomes

$$\begin{aligned} \frac{\partial \hat{m}_1}{\partial t} = & -\gamma\mu_0[\hat{m}_1 \times \bar{H}_{eff}] \\ & + \alpha \left[\hat{m}_1 \times \frac{\partial \hat{m}_1}{\partial t} \right] + \frac{\bar{I}_{13\perp}}{eN_s} \end{aligned} \quad (38)$$

$$\begin{aligned} \frac{\partial \hat{m}_2}{\partial t} = & -\gamma\mu_0[\hat{m}_2 \times \bar{H}_{eff}] \\ & + \alpha \left[\hat{m}_2 \times \frac{\partial \hat{m}_2}{\partial t} \right] + \frac{\bar{I}_{23\perp}}{eN_s} \end{aligned} \quad (39)$$

where the voltages are derived from (37), at the bottom of the page.

We numerically simulated the spin logic circuit using self-consistent solution of the spin-MNA equation with stochastic LLG equation. The specific parameters used for this example are shown in Table V for reproducing the results. We note that the stochastic nature of the LLG equations produces an inherent variability in operation of spin devices.

We show the basic operation of the device in Fig. 10. In Fig. 10(a) we show the dynamics of the magnetic moment of the nanomagnets for various applied voltages. During the time from 0 ns to 2 ns, a positive voltage is applied at node 3, the device acts as an inverting gate during this interval. The device responds over a time of ~ 0.5 ns and the output flips to a state opposite of the input magnet. At 2 ns, the supply voltage is flipped to -5 mV, the device acts as a noninverting gate in this interval and the output responds at ~ 2.5 ns and flips to a state parallel to the input magnet. The trajectories of the magnets' magnetic moments are shown in Fig. 10(b).

We now extract the spin and charge currents via the circuit using the branch conductances. The spin currents along the nanomagnet easy axis are shown in Fig. 11(a). The total energy dissipation of the device can now be calculated as the total charge current sourced from the supply times the supply voltage. The total electrical power of the device can be extracted as shown in Fig. 11(b).

This circuit modeling described here shows that the spin logic circuit, using Table V parameters, operates with transient energy-delay metrics of ~ 30 fJ/bit with up to a 2 GHz response and with zero leakage power (ignoring the overheads). Compared to a CMOS technology with a 60 nW/transistor idle power [74], nonvolatile spin logic with ~ 0 W state hold power and 7

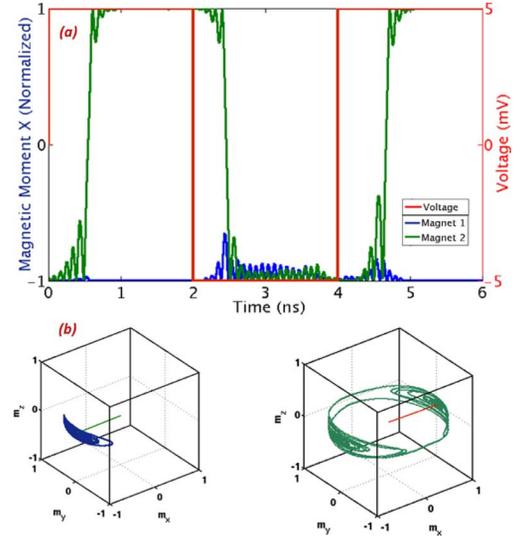


Fig. 10. (a) Transient self-consistent simulation of a spin circuit device. (b) Trajectory of the magnetic moment of the nanomagnets.

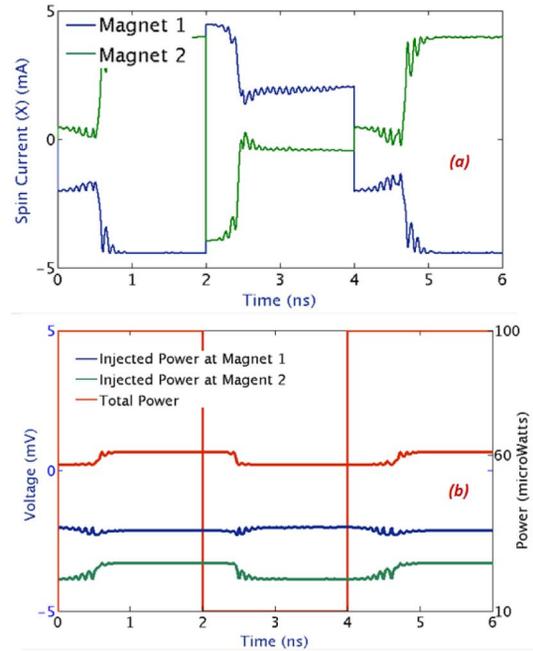


Fig. 11. (a) Spin current via input and output magnets. (b) The instantaneous power through the spin logic device.

year retention time will outperform CMOS logic in idle power by several orders of magnitude. We note the leakage power of the MPU units have approached 50% over the past few years [75]. For transient energy-delay improvement, a physically realistic simulation framework, along with improved spin-electrical

$$\begin{bmatrix} O \\ O \\ O \\ O \\ V_{sp} \end{bmatrix} = \begin{bmatrix} G_{FM1} + G_{sh1} + G_{Se} & -G_{Se\pi} & -G_{FM1} & O & O \\ -G_{Se\pi} & G_{FM2} + G_{sf\pi} + G_{Se\pi} & -G_{FM2} & O & O \\ -G_{FM1} & -G_{FM2} & G_{FM1} + G_{FM2} & O & I \\ -G_{seT} & O & O & G_{seT} + G_{seT} + G_{sfTt} & O \\ O & O & I & O & O \end{bmatrix} \begin{bmatrix} V_1 \\ V_2 \\ V_3 \\ V_4 \\ I_{sp} \end{bmatrix}. \quad (37)$$

transduction methods, materials, and anisotropy engineering, can address this gap in developing device and circuit topologies to approach the fundamental performance of the spintronic devices and circuits for the beyond CMOS era.

IX. CONCLUSION

In summary, we describe a spin circuit framework combining spin transport with generalized modified node analysis to enable SPICE for spintronic circuit analysis. The framework handles both magnetic and nonmagnetic components with the commonly used MNA methodology. This framework will enable study of spintronics for logic, interconnect, memory, and hybrid integration of spin devices with CMOS circuits. The ability to synthesize and analyze spintronic CMOS integrated circuits will enable and accelerate the study of spintronics with the potential attributes of nonvolatility, superior energy-delay, higher logical efficiency, reconfigurability, and suitability for novel computational architectures and logic-memory paradigms.

APPENDIX A

G-MATRIX ELEMENTS OF A NONMAGNETIC DISTRIBUTED CHANNEL

Spin-dependence conduction can be described in a continuous medium (as opposed to a lumped-element circuit) via the drift-diffusion equations [35]. In the following, we provide the derivation of normal magnet (NM) spin conductance matrix introduced in [18]. We show the spin conductances for currents along one direction (x) and one direction (s) of spin, and then generalize it to arbitrary directions of spins. Then the drift-diffusion equations for the current density J , spin current density J_s , voltage V , and spin voltage V_s in a nonmagnetic material are

$$J = \sigma \frac{dV}{dx} \quad (A1)$$

$$J_s = \sigma \frac{dV_s}{dx} \quad (A2)$$

Current continuity implies (Kirchhoff current law)

$$\frac{dJ}{dx} = 0 \quad (A3)$$

$$\frac{dJ_s}{dx} = \frac{\sigma}{\lambda^2} V_s \quad (A4)$$

where the conductivity is σ , and the spin diffusion length is $\lambda = \sqrt{D\tau_{sf}}$. A general solution for these equations is $J = \text{Constant}$

$$V_s = a \exp\left(\frac{x}{\lambda}\right) + b \exp\left(-\frac{x}{\lambda}\right) \quad (B5)$$

$$J_s = \frac{\sigma a}{\lambda} \exp\left(\frac{x}{\lambda}\right) - \frac{\sigma b}{\lambda} \exp\left(-\frac{x}{\lambda}\right) \quad (A5)$$

where the boundary conditions set the coefficients a, b . The specific solution for the uniform conductor of length L is

$$J = \frac{\sigma(V_2 - V_1)}{L} \quad (A6)$$

$$V_{s1} = \frac{a}{E} + bE \quad (A7)$$

$$V_{s2} = aE + \frac{b}{E} \quad (A8)$$

$$J_{s,in} = \frac{\sigma a}{\lambda E} - \frac{\sigma b E}{\lambda} \quad (A9)$$

$$J_{s,out} = \frac{\sigma a E}{\lambda} - \frac{\sigma b}{\lambda E} \quad (A10)$$

where we designate $E = \exp(L/(2\lambda))$;

From these equations, and for the cross-sectional area A of the conductor

$$G = \frac{\sigma A}{L} \quad (A11)$$

According to the above notation, the following relations must be satisfied for the Π -network

$$J_{s,in} = J_{sp} - J_{s1} \quad (A12)$$

$$J_{s,out} = J_{sp} + J_{s2} \quad (A13)$$

$$AJ_{s1} = G_{sf\pi} V_{s1} \quad (A14)$$

$$AJ_{s2} = G_{sf\pi} V_{s2} \quad (A15)$$

$$AJ_{p,s} = G_{se\pi}(V_{s2} - V_{s1}) \quad (A16)$$

for all values of the boundary conditions (specified by a, b). Taking a special case of $a = 0, b = 1$, we arrive at the equations

$$\frac{A\sigma E}{\lambda} = G_{se\pi} \left(E - \frac{1}{E} \right) + G_{sf\pi} E \quad (A17)$$

$$\frac{A\sigma}{\lambda E} = G_{se\pi} \left(E - \frac{1}{E} \right) - G_{sf\pi} \frac{1}{E} \quad (A18)$$

which have the solution

$$G_{se\pi} = \frac{\sigma A}{\lambda} \cos \text{ech} \left(\frac{L}{\lambda} \right) \quad (A19)$$

$$G_{sf\pi} = \frac{\sigma A}{\lambda} \tanh \left(\frac{L}{2\lambda} \right). \quad (A20)$$

Hence, the conductance for the series branch of the Π network is [18]

$$G_{se\pi} = \begin{bmatrix} \frac{A_{ch}}{\rho_{ch} L_c} & 0 & 0 & 0 \\ 0 & \frac{A_{ch}}{\rho_{ch} \lambda_s} \csc h\left(\frac{L_c}{\lambda_s}\right) & 0 & 0 \\ 0 & 0 & \frac{A_{ch}}{\rho_{ch} \lambda_s} \csc h\left(\frac{L_c}{\lambda_s}\right) & 0 \\ 0 & 0 & 0 & \frac{A_{ch}}{\rho_{ch} \lambda_s} \csc h\left(\frac{L_c}{\lambda_s}\right) \end{bmatrix}. \quad (A21)$$

The conductance for the parallel branch of the Π network is

$$G_{sf\pi} = \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & \frac{A_{ch}}{\rho_{ch} \lambda_s} \tanh\left(\frac{L_c}{2\lambda_s}\right) & 0 & 0 \\ 0 & 0 & \frac{A_{ch}}{\rho_{ch} \lambda_s} \tanh\left(\frac{L_c}{2\lambda_s}\right) & 0 \\ 0 & 0 & 0 & \frac{A_{ch}}{\rho_{ch} \lambda_s} \tanh\left(\frac{L_c}{2\lambda_s}\right) \end{bmatrix}. \quad (A22)$$

APPENDIX B

II TO T EQUIVALENT CIRCUIT TRANSFORMATION

Spin-dependent conductance with spin relaxation can be equivalently represented by Π -shaped [Fig. 4(b)] or T-shaped [Fig. 4(c)] networks. Here we derive a general relation between these two approaches.

In both cases the vector voltages at the terminals, V_1 and V_2 are the same. In the Π -network, the spin-relaxation currents are

$$I_1 = G_{sf\pi} V_1 \quad (B1)$$

$$I_2 = G_{sf\pi} V_2 \quad (B2)$$

and the current passing through the center conductance is

$$I_p = G_{se\pi}(V_2 - V_1). \quad (\text{B3})$$

They are related to the total currents entering and leaving the network as follows:

$$I_{in} = I_p - I_1 \quad (\text{B4})$$

$$I_{out} = I_p + I_2. \quad (\text{B5})$$

Using these relations in (C4), (C5) is convenient to express the sum and the difference of the in- and out-currents.

$$I_{out} - I_{in} = G_{sf\pi}(V_2 + V_1) \quad (\text{B6})$$

$$I_{out} + I_{in} = (2G_{se\pi} + G_{sf\pi})(V_2 - V_1). \quad (\text{B7})$$

Similarly we obtain for the T-network; the current from the middle node to the ground is related to the vector voltage at this node

$$I_m = G_{sf\pi}V_m \quad (\text{B8})$$

and the in- and out-currents are

$$I_{in} = G_{seT}(V_m - V_1) \quad (\text{B9})$$

$$I_{out} = G_{seT}(V_2 - V_m). \quad (\text{B10})$$

The current conservation results in

$$I_{out} = I_{in} + I_m. \quad (\text{B11})$$

The voltage at the middle node is thus related (for nonzero spin relaxation) to the in- and out-currents

$$V_m = G_{seT}^{-1}(I_{out} - I_{in}). \quad (\text{B12})$$

As before, we express via the unity matrix I

$$(I + 2G_{seT}G_{sfT}^{-1})(I_{out} - I_{in}) = G_{seT}(V_2 + V_1) \quad (\text{B13})$$

$$I_{out} + I_{in} = G_{seT}(V_2 - V_1). \quad (\text{B14})$$

Since the two expressions for in- and out-currents should be equivalent, the following relations between nonzero conductances must hold:

$$G_{seT} = 2G_{se\pi} + G_{sf\pi} \quad (\text{B15})$$

$$G_{sf\pi}^{-1} = G_{seT}^{-1} + 2G_{sfT}^{-1}. \quad (\text{B16})$$

Simple algebraic manipulations permit the expression of the T-conductances in terms on π -conductances

$$G_{sfT} = G_{sf\pi}G_{se\pi}^{-1}G_{sf\pi} + 2G_{sf\pi}. \quad (\text{B17})$$

Note that this derivation is not trivially reduced to the results of the traditional electronic network theory, because the conductances are matrices rather than scalars.

APPENDIX C

G-MATRIX ELEMENTS OF FM EXPRESSED AS SPIN REFLECTION AND TRANSMISSION COEFFICIENTS

4×4 CONDUCTION MATRIX ELEMENTS OF A FERROMAGNETIC METAL IN CONTACT WITH A NORMAL METAL EXPRESSED IN TERMS OF SPIN REFLECTION AND TRANSMISSION COEFFICIENTS: Earlier, we described the

conduction matrix elements of a FM as phenomenological constants extracted from experimental properties of the FM-NM conduction. Here, we provide a description of the conduction matrix elements of a FM that is derived from an *ab initio* approach [18], [38]. The conduction matrix

$$G_{FM} = \begin{bmatrix} G & \alpha G & 0 & 0 \\ \alpha G & G & 0 & 0 \\ 0 & 0 & G_{SL} & G_{FL} \\ 0 & 0 & -G_{FL} & G_{SL} \end{bmatrix} \quad (\text{C1})$$

can also be written in terms of spin scattering conductance elements as

$$G_{FM} = \begin{bmatrix} G_{\uparrow\uparrow} + G_{\downarrow\downarrow} & G_{\uparrow\uparrow} - G_{\downarrow\downarrow} & 0 & 0 \\ G_{\uparrow\uparrow} - G_{\downarrow\downarrow} & G_{\uparrow\uparrow} + G_{\downarrow\downarrow} & 0 & 0 \\ 0 & 0 & 2\text{Re}G_{\uparrow\downarrow} & 2\text{Im}G_{\uparrow\downarrow} \\ 0 & 0 & -2\text{Im}G_{\uparrow\downarrow} & 2\text{Re}G_{\uparrow\downarrow} \end{bmatrix} \quad (\text{C2})$$

where $G_{\uparrow\uparrow}$, $G_{\downarrow\downarrow}$ and $G_{\uparrow\downarrow}$ are the matrix elements derived from spin scattering at the FM-NM interface. The conduction matrix elements of an FM-NM interface can be described in terms of the reflection and transmission properties of the spin up and spin down electrons incident from an NM to FM [38].

$$G^{\uparrow\uparrow} = \frac{e^2}{h} \sum_{n \in NM} \sum_{m \in FM} |t_{\uparrow}^{nm}|^2 \quad (\text{C3})$$

$$G^{\downarrow\downarrow} = \frac{e^2}{h} \sum_{n \in NM} \sum_{m \in FM} |t_{\downarrow}^{nm}|^2 \quad (\text{C4})$$

$$G^{\uparrow\downarrow} = G^{\downarrow\uparrow*} = \frac{e^2}{h} \sum_{n \in NM} \left(1 - \sum_{m \in NM} r_{\uparrow}^{nm} r_{\downarrow}^{nm*} \right) \quad (\text{C5})$$

where e^2/h is the conductance per spin of a ballistic channel with ideal contacts [38]; t_{\uparrow}^{nm} , t_{\downarrow}^{nm} are the transmission coefficients for up and down spin electrons from NM to FM; r_{\uparrow}^{nm} , r_{\downarrow}^{nm} are the reflection coefficients of the up and down spin electrons at the FM-NM interface; n is the number of modes in the NM, m is the number of modes in the FM. The number of modes in a metal NM can in-turn be written from the metal's Fermi wave vector k_f [48].

It has been argued that r_{\uparrow}^{nm} , r_{\downarrow}^{nm} are close to zero for many material systems [38], which simplifies the spin torque conductance to G_{SL} to

$$G_{SL} = 2\text{Re} G^{\uparrow\downarrow} = \frac{2e^2}{h}n = \frac{2e^2}{h} \frac{Ak_f^2}{4\pi}. \quad (\text{C6})$$

APPENDIX D

G-MATRIX OF FREE FM LAYER

DERIVATION FOR FREE MAGNETIC LAYER CONDUCTION MATRIX: Here we derive the G-Matrix for a free layer FM with an arbitrary magnetic moment direction (\hat{m}) (see Fig. 7). Let φ be the angle of the magnetic moment with z-axis and θ be the angle of the projection of (\hat{m}), with x-axis. We can write \hat{m} in co-ordinate system xyz as

$$\hat{m} = \cos \theta \sin \varphi \hat{x} + \sin \theta \sin \varphi \hat{y} + \cos \varphi \hat{z}. \quad (\text{D1})$$

Let us choose a new coordinate system XYZ such that the \hat{m} is collinear with the new X-axis. (Refer to Section V)

$$\hat{X} = \hat{m} \quad (\text{D2})$$

$$\hat{Y} = - \frac{(\hat{X} \times \hat{x})}{\|\hat{X} \times \hat{x}\|} \quad (\text{D3})$$

$$\hat{Z} = \hat{X} \times \hat{Y}. \quad (\text{D4})$$

In the new coordinate system

$$\vec{I}_{XYZ} = G_{FN}(\hat{x})\vec{V}_{XYZ} \quad (\text{D5})$$

where G_0 is the matrix described in Section V. Let us substitute

$$\vec{I}_{XYZ} = R\vec{I}_{xyz} \quad (\text{D6})$$

$$\vec{V}_{XYZ} = R\vec{V}_{xyz} \quad (\text{D7})$$

and rearrange to obtain the current, voltage relation in the xyz co-ordinate system. We obtain

$$\vec{I}_{xyz} = R^{-1}G_{FN}(\hat{x})R\vec{V}_{xyz}. \quad (\text{D8})$$

Hence, the conductance matrix for an FM with magnetic moment along an arbitrary direction is given by

$$G_{FN}(\hat{m}) = R^{-1}(\hat{m})G_{FN}(\hat{x})R(\hat{m}). \quad (\text{D9})$$

APPENDIX E

CONVERSION FROM SPINOR BASIS TO VECTOR BASIS

Conversion Between Spinor Spin Current/Voltage Basis to 4 Component Vector Current/Voltage Basis: The derivations for magneto-electronic circuit theory are often performed in the spinor basis for the electrons [39]. For convenience we list the conversion from spinor basis to Cartesian vector basis as well as 4-component current basis. The current in a spinor basis can be written as [39]

$$\hat{I} = \frac{(I_c \hat{1} + \vec{I}_s \cdot \vec{\sigma})}{2} \quad (\text{E1})$$

where $\vec{\sigma}$ is the Pauli spin matrix

$$\vec{\sigma} = \sigma_x \hat{x} + \sigma_y \hat{y} + \sigma_z \hat{z} \quad (\text{E2})$$

which yields

$$\hat{I} = \begin{bmatrix} I_{\uparrow\uparrow} & I_{\uparrow\downarrow} \\ I_{\downarrow\uparrow} & I_{\downarrow\downarrow} \end{bmatrix} = \begin{bmatrix} I_c + I_z & I_x - iI_y \\ I_x + iI_y & I_c - I_z \end{bmatrix}. \quad (\text{E3})$$

Hence, the 4-component current vector can be derived from the spinor current as follows:

$$\vec{I} = \frac{1}{2} \begin{bmatrix} I_{\uparrow\uparrow} + I_{\downarrow\downarrow} \\ I_{\uparrow\downarrow} + I_{\downarrow\uparrow} \\ i(I_{\uparrow\downarrow} - I_{\downarrow\uparrow}) \\ I_{\uparrow\uparrow} - I_{\downarrow\downarrow} \end{bmatrix}. \quad (\text{E4})$$

Similarly, 4-component vector spin voltage can be derived from spinor voltage as follows:

$$\vec{V} = \frac{1}{2} \begin{bmatrix} V_{\uparrow\uparrow} + V_{\downarrow\downarrow} \\ V_{\uparrow\downarrow} + V_{\downarrow\uparrow} \\ i(V_{\uparrow\downarrow} - V_{\downarrow\uparrow}) \\ V_{\uparrow\uparrow} - V_{\downarrow\downarrow} \end{bmatrix}. \quad (\text{E5})$$

APPENDIX F

STOCHASTIC LLG EQUATIONS

THERMAL NOISE OF NANOMAGNETS: The dynamics of nanomagnets are strongly affected by the thermal noise. Thermal noise in a nanomagnet manifests as fluctuations to the internal anisotropic field [51]–[54]. The thermal noise can be considered as a result of the microscopic degrees of freedom of the conduction electrons and the lattice of the ferromagnetic element [51].

At room temperature T , the thermal noise is described by a Gaussian white noise (with a time domain Dirac-delta auto-correlation). The noise field acts isotropically on the magnet. In presence of the noise, the LLG equation can be written as

$$\frac{\partial \hat{m}}{\partial t} = -\gamma \mu_0 [\hat{m} \times \bar{H}_{eff}(T)] + \alpha \left[\hat{m} \times \frac{\partial \hat{m}}{\partial t} \right] + \frac{\vec{I}_\perp}{eN_s} \quad (\text{F1})$$

where we modified (33) by adding temperature dependence. The internal field is described as

$$\bar{H}_{eff}(T) = \bar{H}_{eff} + (H_i \hat{x} + H_j \hat{y} + H_k \hat{z}) \quad (\text{F2})$$

$$\langle H_l(t) \rangle = 0 \quad (\text{F3})$$

$$\langle H_l(t) H_k(t') \rangle = \frac{2\alpha k_B T}{\mu_0^2 \gamma M_s V} \delta(t - t') \delta_{lk}. \quad (\text{F4})$$

The initial conditions of the magnets should also be randomized to be consistent with the distribution of initial angles of magnet moments in a large collection of magnets. At temperature T , the initial angle of the magnets follows [52]

$$\langle \theta^2 \rangle = \frac{kT}{M_s V \mu_0 H_{ani}}. \quad (\text{F5})$$

NUMERICAL METHODS FOR STOCHASTIC LLG EQUATIONS: An accurate choice of the method for integration of the stochastic LLG equation is essential since: a) the stochastic differential equations (SDE) require careful handling of the order of integration [67], [68]; b) a multiplicative white noise requires an appropriate choice of calculus [53], [69]. The appropriate model for direct integration of SDE are usually first order integration methods such as Euler & Heun. Even though higher order methods have been proposed in the literature, the accuracy and applicability for realistic SDEs have been questioned [67], [68]. Hence, a first order integration method with a fixed time step is generally recommended [53].

Secondly, Stratonovich calculus is used for interpreting the multiplicative white noise. We used a mid-point integration method [53] to apply the Stratonovich calculus while integrating the LLG equation. The discretized integration rule is

$$y_{n+1} = y_n + \Delta t \cdot f \left(t_n + \frac{\Delta t}{2}, y_n + \frac{\Delta t}{2} \cdot f(t_n, y_n) \right) \quad (\text{F6})$$

where $y = d\hat{m}/dt$. The variance of the noise varies depending on the time step size. The discretization was performed internally using a matlab implicit self-consistent solver.

APPENDIX G

EQUIVALENT CONDUCTANCES SERIES AND PARALLEL

The equivalent conductance of a two spin conductance elements connected in parallel is

$$G_{\parallel} = G_1 + G_2 \quad (\text{G1})$$

$$G_{se} = (G_1 + G_2)^{-1} G_1 G_2. \quad (\text{G2})$$

The rule for voltage division says, voltage across conductance $i = 1, 2$ is

$$\Delta V_i = (G_1 + G_2)^{-1} G_i V. \quad (\text{G3})$$

APPENDIX H

WRITING SPIN MNA EQUATIONS, NOTATIONS

Rules for writing the spin MNA equations are very similar to the rules for regular MNA [63], [65]. However, care should be taken to include the spin current conservation law described in Section III. Also, 4×4 identity matrices are to be used where appropriate to indicate the voltage sources.

To apply the spin-MNA method to a circuit with n nodes with m voltage sources, the steps are:

1. *Number the nodes*: Select a reference node (usually the ground, numbered 0) and name the remaining $n-1$ nodes. Also label currents through each current source.
2. *Name the currents*: Assign a name to the current through each voltage source flowing from positive node to negative node of the source.
3. *Apply spin KCL*: Apply Spin current conservation law at each node with current into the node to be positive.
4. Write an equation for spin voltage at each spin voltage source.
5. Rearrange the equations to the form of (36).
6. Invert the \mathbf{A} matrix and multiply with \mathbf{Z} to obtain the voltages of the nodes and current of the sources.
7. Using conductance matrix to calculate the relevant currents.

APPENDIX I

COMMENT ON EXTENSION FOR SEMICONDUCTOR SPIN TRANSPORT

We briefly comment on the extendibility of the present approach to semiconductor spin transport [70]. The major modifications required to handle semiconductors using conduction matrix approach are as follows: a) conductance matrix for describing drift-diffusion spin current [71]; b) description of dilute magnetic semiconductors; c) appropriate definition of spin currents in presence of spin orbit coupling [72], [73].

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REFERENCES

- [1] S. A. Wolf *et al.*, "Spintronics: A spin based electronics vision for the future," *Science*, vol. 294, pp. 1488–1495, 2001.
- [2] C. Chappert, A. Fert, and F. N. Van Dau, "The emergence of spintronics in data storage," *Nature Mater.*, vol. 6, pp. 813–813, 2007.
- [3] "Report on ITRS emerging research devices," [Online]. Available: http://www.itrs.net/links/2009itrs/2009chapters_2009tables/2009_ERD.pdf
- [4] K. Bernstein, R. K. Cavin, W. Porod, A. Seabaugh, and J. Welsler, "Device and architecture outlook for beyond CMOS switches," *Proc. IEEE*, vol. 98, no. 12, pp. 2169–2184, Dec. 2010.
- [5] S. A. Wolf, L. Jiwei, M. R. Stan, E. Chen, and D. M. Treger, "The promise of nanomagnetism and spintronics for future logic and universal memory," *Proc. IEEE*, vol. 98, no. 12, pp. 2155–2168, Dec. 2010.
- [6] S. Datta and B. Das, "Electronic analog of the electro-optical modulator," *Appl. Phys. Lett.*, vol. 56, no. 7, pp. 665–667, 1990.
- [7] R. P. Cowburn and M. E. Welland, "Room temperature magnetic quantum cellular automata," *Science*, vol. 287, no. 5457, pp. 1466–1468, 2000.
- [8] A. Ney, C. Pampuch, R. Koch, and K. H. Ploog, "Programmable computing with a single magnetoresistive element," *Nature*, vol. 425, no. 6957, pp. 485–487, 2003.
- [9] S. Sugahara and M. Tanaka, "A spin metal-oxide-semiconductor field-effect transistor using half-metallic-ferromagnet contacts for the source and drain," *Appl. Phys. Lett.*, vol. 84, no. 13, pp. 2307–2309, 2004.
- [10] D. E. Nikonov and G. I. Bourianoff, "Spin gain transistor in ferromagnetic semiconductors: The semiconductor Bloch equations approach," *IEEE Trans. Nanotechnol.*, vol. 4, no. 2, pp. 206–214, 2005.
- [11] J. Wang, H. Meng, and J.-P. Wang, "Programmable spintronics logic device based on a magnetic tunnel junction element," *J. Appl. Phys.*, vol. 97, no. 10, pp. 10D509-1–10D509-3, 2005.
- [12] D. A. Allwood, G. Xiong, C. C. Faulkner, D. Atkinson, D. Petit, and R. P. Cowburn, "Magnetic domain-wall logic," *Science*, vol. 309, no. 5741, pp. 1688–1692, 2005.
- [13] A. Khitun and K. L. Wang, "Nano scale computational architectures with spin wave bus," *Superlatt. Microstruct.*, vol. 38, no. 3, pp. 184–200, 2005.
- [14] S. Lee, S. Choa, S. Lee, and H. Shin, "Magneto-logic device based on a single-layer magnetic tunnel junction," *IEEE Trans. Electron Devices*, vol. 54, no. 8, pp. 2040–2044, 2007.
- [15] S. Matsunaga *et al.*, "Fabrication of a nonvolatile full adder based on logic-in-memory architecture using magnetic tunnel junctions," *Appl. Phys. Exp.*, vol. 1, pp. 091301-1–091301-3, 2008.
- [16] B. Behin-Aein, D. Datta, S. Salahuddin, and S. Datta, "Proposal for an all-spin logic device with built-in memory," *Nat. Nanotechnol.*, vol. 5, no. 4, pp. 266–270, 2010.
- [17] D. E. Nikonov, G. I. Bourianoff, and T. Ghani, "Proposal of a spin torque majority gate logic," *IEEE Electron Device Lett.*, vol. 32, no. 8, pp. 1128–1130, Aug. 2011.
- [18] B. Behin-Aein, A. Sarkar, S. Srinivasan, and S. Datta, "Switching energy-delay of all spin logic devices," *Appl. Phys. Lett.*, vol. 98, pp. 123510-1–123510-3, 2011.
- [19] S. Srinivasan, A. Sarkar, B. Behin-Aein, and S. Datta, "All spin logic device with inbuilt nonreciprocity," *IEEE Trans. Magn.*, vol. 47, no. 10, pp. 4026–4032, Oct. 2011.
- [20] S. Borkar and A. A. Chien, "The future of microprocessors," *Commun. ACM*, vol. 55, no. 5, pp. 67–77, May 2011.
- [21] V. Saripalli, G. Sun, A. Mishra, Y. Xie, S. Datta, and V. Narayanan, "Exploiting heterogeneity for energy efficiency in chip multiprocessors," *IEEE J. Emerg. Sel. Topics Circuits Syst.*, vol. 1, no. 2, pp. 109–119, Jun. 2011.
- [22] C. Augustine, G. Panagopoulos, B. Behin-Aein, S. Srinivasan, A. Sarkar, and K. Roy, "Low-power functionality enhanced computation architecture using spin-based devices," in *Proc. IEEE/ACM Int. Symp. Nanoscale Archit. (NANOARCH)*, Jun. 8–9, 2011, pp. 129–136.
- [23] R. Venkatesan, V. K. Chippa, C. Augustine, K. Roy, and A. Raghunathan, "Energy efficient many-core processor for recognition and mining using spin-based memory," in *Proc. IEEE/ACM Int. Symp. Nanoscale Archit. (NANOARCH)*, Jun. 8–9, 2011, pp. 122–128.
- [24] M. Hosomi, H. Yamagishi, T. Yamamoto, K. Bessho, Y. Higo, K. Yamane, H. Yamada, M. Shoji, H. Hachino, C. Fukumoto, H. Nagao, and H. Kano, "A novel nonvolatile memory with spin torque transfer magnetization switching: Spin-RAM," *IEDM Tech. Dig.*, pp. 459–462, Dec. 2005.

- [25] T. Kawahara, R. Takemura, K. Miura, J. Hayakawa, S. Ikeda, Y. Lee, R. Sasaki, Y. Goto, K. Ito, I. Meguro, F. Matsukura, H. Takahashi, H. Matsuoaka, and H. Ohno, "2 Mb spin-transfer torque RAM (SPRAM) with bit-by-bit bidirectional current write and parallelizing-direction current read," *ISSCC Tech. Dig.*, pp. 480–481, Feb. 2007.
- [26] R. Beach *et al.*, "A statistical study of magnetic tunnel junctions for high-density spin torque transfer-MRAM (STT-MRAM)," *IEDM Tech. Dig.*, pp. 306–308, Dec. 2008.
- [27] T. Kishi *et al.*, "Lower-current and fast switching of a perpendicular TMR for high speed and high density spin-transfer-torque MRAM," *IEDM Tech. Dig.*, pp. 309–312, Dec. 2008.
- [28] K. Ono *et al.*, "SPRAM with large thermal stability for high immunity to read disturbance and long retention for high-temperature operation," in *Symp. VLSI Tech. Dig.*, Jun. 2009, pp. 228–229.
- [29] K. Lee and S. H. Kang, "Development of embedded STT-MRAM for Mobile system-on-chips," *IEEE Trans. Magn.*, vol. 47, no. 1, pp. 131–136, Jan. 2011.
- [30] A. Driskill-Smith *et al.*, "Latest advances and roadmap for in-plane and perpendicular STT-RAM," in *Proc. 3rd IEEE Int. Memory Workshop (IMW)*, May 22–25, 2011, pp. 1–3.
- [31] T. Yang, T. Kimura, and Y. Otani, "Giant spin-accumulation signal and pure spin-current-induced reversible magnetization switching," *Nat. Phys.*, vol. 4, pp. 851–854, 2008.
- [32] J. Z. Sun *et al.*, "A three-terminal spin-torque-driven magnetic switch," *App. Phys. Lett.*, vol. 95, pp. 083506.1–083506.3, Aug. 2009.
- [33] A. Vogel, J. Wulffhorst, and G. Meier, "Enhanced spin injection and detection in spin valves with integrated tunnel barrier," *Appl. Phys. Lett.*, vol. 94, pp. 122510–122510, 2009.
- [34] Y. Fukuma, L. Wang, H. Idzuchi, S. Takahashi, S. Maekawa, and Y. Otani, "Giant enhancement of spin accumulation and long-distance spin precession in metallic lateral spin valves," *Nat. Mater.*, vol. 10, pp. 527–531, 2011.
- [35] T. Valet and A. Fert, "Theory of the perpendicular magnetoresistance in magnetic multilayers," *Phys. Rev. B*, vol. 48, pp. 7099–7113, 1993.
- [36] A. Brataas, Y. V. Nazarov, and G. E. W. Bauer, "Finite element theory of transport in ferromagnet-normal metal systems," *Phys. Rev. Lett.*, vol. 84, pp. 2481–2484, 2000.
- [37] M. D. Stiles and A. Zangwill, "Anatomy of spin-transfer torque," *Phys. Rev. B*, vol. 66, pp. 014407-1–014407-14, 2002.
- [38] A. Brataas, G. E. W. Bauer, and P. J. Kelly, "Non-collinear magneto-electronic," *Phys. Rep.*, vol. 427, no. 4, pp. 157–255, Apr. 2006.
- [39] C. Cohen-Tannoudji, B. Diu, and F. Laloe, *Quantum Mechanics*. Weinheim, Germany: Wiley-VCH, 1977, vol. 2.
- [40] M. D. Stiles, "Spin-dependent interface transmission and reflection in magnetic multilayers," *J. Appl. Phys.*, vol. 79, pp. 5805–5810, 1996.
- [41] D. C. Ralph and M. D. Stiles, "Spin transfer torques," *J. Magn. Magn. Mater.*, vol. 320, pp. 1190–1190, 2008.
- [42] M. Cahay, M. McLennan, and S. Datta, "Conductance of an array of elastic scatterers: A scattering-matrix approach," *Phys. Rev. B*, vol. 37, pp. 10125–10136, 1988.
- [43] Y. V. Nazarov, "Circuit theory of andreev conductance," *Phys. Rev. Lett.*, vol. 73, pp. 1420–1423, 1994.
- [44] S. Dubois *et al.*, "Evidence for a short spin diffusion length in permalloy from the giant magnetoresistance of multilayered nanowires," *Phys. Rev. B*, vol. 60, pp. 477–484, 1999.
- [45] J. Bass and W. P. Pratt Jr., "Spin-diffusion lengths in metals and alloys, and spin-flipping at metal/metal interfaces: An experimentalist's critical review," *J. Phys., Condens. Matter*, vol. 19, pp. 183201-1–183201-50, 2007.
- [46] T. Kimura and Y. Otani, "Large spin accumulation in a permalloy-silver lateral spin valve," *Phys. Rev. Lett.*, vol. 99, pp. 196604-1–196604-4, 2007.
- [47] T. Kimura, T. Sato, and Y. Otani, "Temperature evolution of spin relaxation in NiFe/Cu lateral spin valve," *Phys. Rev. Lett.*, vol. 100, pp. 066602-1–066602-4, 2008.
- [48] C. Kittel, *Introduction to Solid State Physics*, 8th ed. Hoboken, NJ: Wiley, 2005.
- [49] J. C. Slonczewski, "Current-driven excitations of magnetic multilayers," *J. Magn. Magn. Mater.*, vol. 159, pp. L1–L7, 1996.
- [50] L. Berger, "Emission of spin waves by a magnetic multilayer traversed by a current," *Phys. Rev. B*, vol. 54, pp. 9353–9358, 1996.
- [51] W. F. Brown, "Thermal fluctuations of a single-domain particle," *Phys. Rev.*, vol. 130, pp. 1677–1686, 1963.
- [52] J. Z. Sun, "Spin angular momentum transfer in current-perpendicular nanomagnetic junctions," *IBM J. Res. Develop.*, vol. 50, no. 1, pp. 81–100, Jan. 2006.
- [53] M. d'Aquino, C. Serpico, G. Coppola, I. D. Mayergoyz, and G. Bertotti, "Midpoint numerical technique for stochastic Landau-Lifshitz-Gilbert dynamics," *J. Appl. Phys.*, vol. 99, no. 8, pp. 08B905-1–08B905-3.
- [54] F. M. Spedalieri, A. P. Jacob, D. E. Nikonov, and V. P. Roychowdhury, "Performance of magnetic quantum cellular automata and limitations due to thermal noise," *IEEE Trans. Nanotechnol.*, vol. 10, no. 3, pp. 537–546, May 2011.
- [55] Support—Differential equations in MATLAB [Online]. Available: www.mathworks.com/support/tech-notes/1500/1510.html#implic
- [56] S. Y. Jang, C.-Y. You, S. H. Lim, and S. R. Lee, "Annealing effects on the magnetic dead layer and saturation magnetization in unit structures relevant to a synthetic ferrimagnetic free structure," *J. Appl. Phys.*, vol. 109, no. 1, pp. 013901-1–013901-5, Jan. 2011.
- [57] C. Bilzer, T. Devolder, K. Joo-Von, G. Counil, C. Chappert, S. Cardoso, and P. P. Freitas, "Study of the dynamic magnetic properties of soft CoFeB films," *J. Appl. Phys.*, vol. 100, no. 5, pp. 053903-1–053903-4, Sep. 2006.
- [58] G. D. Fuchs, J. C. Sankey, V. S. Pribrag, L. Qian, P. M. Braganca, A. G. F. Garcia, E. M. Ryan, L. Zhi-Pan, O. Ozatay, D. C. Ralph, and R. A. Buhrman, "Spin-torque ferromagnetic resonance measurements of damping in nanomagnets," *Appl. Phys. Lett.*, vol. 91, no. 6, pp. 062507-1–062507-3, Aug. 2007.
- [59] X. Liu, Z. Wenzhe, M. J. Cartter, and G. Xiao, "Ferromagnetic resonance and damping properties of CoFeB thin films as free layers in MgO-based magnetic tunnel junctions," *J. Appl. Phys.*, vol. 110, no. 3, pp. 033910-1–033910-5, Aug. 2011.
- [60] Y. Kim, S. C. Oh, W. C. Lim, J. H. Kim, W. J. Kim, J. H. Jeong, H. J. Shin, K. W. Kim, K. S. Kim, J. H. Park, S. H. Park, H. Kwon, K. H. Ah, J. E. Lee, S. O. Park, S. Choi, H. K. Kang, and C. Chung, "Integration of 28 nm MJT for 8–16 Gb level MRAM with full investigation of thermal stability," in *Proc. Symp. VLSI Technol. (VLSIT)*, Jun. 14–16, 2011, pp. 210–211.
- [61] M. Munakata, A. Shin-Ichi, and M. yagi, "B-concentration dependence on anisotropy field of CoFeB thin film for gigahertz frequency use," *IEEE Trans. Magn.*, vol. 41, no. 10, pp. 3262–3264, Oct. 2005.
- [62] H. Chung-Wen, A. Ruehli, and P. Brennan, "The modified nodal approach to network analysis," *IEEE Trans. Circuits Syst.*, vol. CAS-22, no. 6, pp. 504–509, Jun. 1975.
- [63] R. A. DeCarlo and P. M. Lin, *Linear Circuit Analysis: Time Domain, Phasor and Laplace Transform Approaches*. New York: Oxford Univ. Press, 2001.
- [64] V. Litovski and M. Zvolinski, *VLSI Circuit Simulation and Optimization*. Norwell, MA: Kluwer Academic, 1997.
- [65] An algorithm for modified nodal analysis [Online]. Available: <http://www.swarthmore.edu/NatSci/echeeve1/Ref/mna/MNA3.html>
- [66] R. Berry, "An optimal ordering of electronic circuit equations for a sparse matrix solution," *IEEE Trans. Circuit Theory*, vol. CT-18, no. 1, pp. 40–50, Jan. 1971.
- [67] J. Wilkie, "Numerical methods for stochastic differential equations," *Phys. Rev. E*, vol. 70, 2004, Art. 017701.
- [68] K. Burrage, P. Burrage, D. Higham, P. Kloeden, and E. Platen, "Comment on Numerical methods for stochastic differential equations," *Phys. Rev. E*, vol. 74, 2006, Art. 068701.
- [69] D. J. Higham, "An algorithmic introduction to numerical simulation of stochastic differential equations," *SIAM Rev.*, vol. 43, p. 525, 2001.
- [70] H. Ohno, "A window on the future of spintronics," *Nat. Mater.*, vol. 9, pp. 952–954, 2010.
- [71] A. M. Roy, D. E. Nikonov, and K. C. Saraswat, "Electric field effects in semiconductor spin transport—A transfer matrix formalism," *IEEE Trans. Magn.*, vol. 47, no. 10, pp. 2746–2749, Oct. 2011.
- [72] J. Shi, P. Zhang, D. Xiao, and Q. Niu, "Proper definition of spin current in spin-orbit coupled systems," *Phys. Rev. Lett.*, vol. 96, 2006, Art. 076604.
- [73] H.-J. Drouhin, G. Fishman, and J.-E. Wegrowe, "Spin currents in semiconductors: Redefinition and counterexample," *Phys. Rev. B*, vol. 83, 2011, Art. 113307.
- [74] U. E. Avci, R. Rios, K. Kuhn, and I. A. Young, "Comparison of performance, switching energy and process variations for the TFET and MOSFET in logic," in *Proc. Symp. VLSI Technol. (VLSIT)*, Jun. 14–16, 2011, pp. 124–125.
- [75] N. Kim *et al.*, "Leakage current: Moore's law meets static power," *Computer*, vol. 36, no. 12, pp. 68–75, 2003.
- [76] S. Salahuddin and S. Datta, "Self-consistent simulation of quantum transport and magnetization dynamics in spin-torque based devices," *Appl. Phys. Lett.*, vol. 15, 2006, Art. 153504.



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